Internship offer - 2027

DLL-based TDC design (Mixed-Signal Microelectronics)

Internship Level : Master 2

Duration : 6 months (5 months mini)

Supervisors : Ludovic Alvado, Laurent Leterrier

Email : alvado@lpccaen.in2p3.fr

Description :

This Analog/Mixed-Signal design internship will focus on a key circuit block of a front-end ASIC for particle detectors. The successful candidate will assist the LPC team to design a time to digital converter using CMOS technology that meets the overall specifications. We are looking for a highly motivated student pursuing an MS in Electrical Engineering with a focus in Analog/Mixed-Signal circuit design. In this internship, you will be responsible for developing schematics and layouts using Cadence tools and searching for improvements in concepts and designs based on the results of simulations. It is useful but not necessary to speak French.
This internship requires a good knowledge of analogue and mixed-mode integrated circuit design and proficiency in Cadence's Virtuoso design flow.
The intern will be assigned to the LPCCaen's microelectronics department and will be supervised by experienced engineers.

If you are interested, please send your application and CV to the following email :
alvado@lpccaen.in2p3.fr

Period and remuneration :

From February 2027 for 5 to 6 months

35 hours per week

15% of the hourly social security ceiling (approximately 634€/month based on the 2025 hourly rate)